

ABSTRACT OF THE DISCLOSURE

A common bias section is composed of a first series circuit having an internal resistor and an external resistor connected in series and
5 an operational amplifier having a first input terminal connected to a reference voltage, a second input terminal connected to a Vr1 node, and an output terminal connected to the series circuit. An impedance trimming section is composed of a series circuit having
10 an internal resistor and an impedance dummy resistor connected in series, a comparator CMP having a first input terminal connected to the Vr1 node and a second input terminal connected to a Vt01 node, a code control circuit which uses a clock signal to latch an output
15 signal from the comparator to generate a plurality of switching codes, and a switching circuit which switch a resistance value of the impedance dummy resistor.